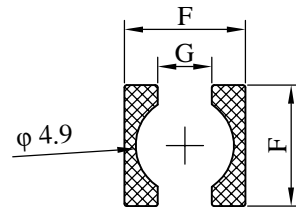
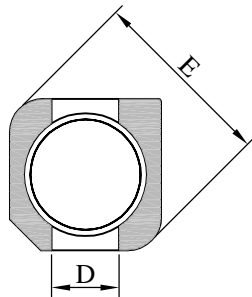
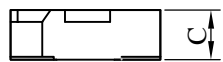
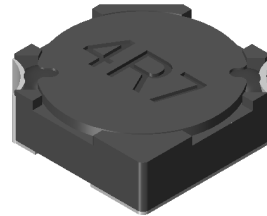
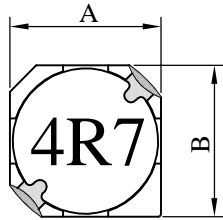


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.		DH5018□□□□F□-□□□		
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I . Configuration and dimensions :



(PCB Pattern)

Unit : m/m

A	B	C	D	E	F	G
6.00 max.	6.00 max.	1.80 max.	2.00 ref.	6.80 ref.	6.20 ref.	1.80 ref.

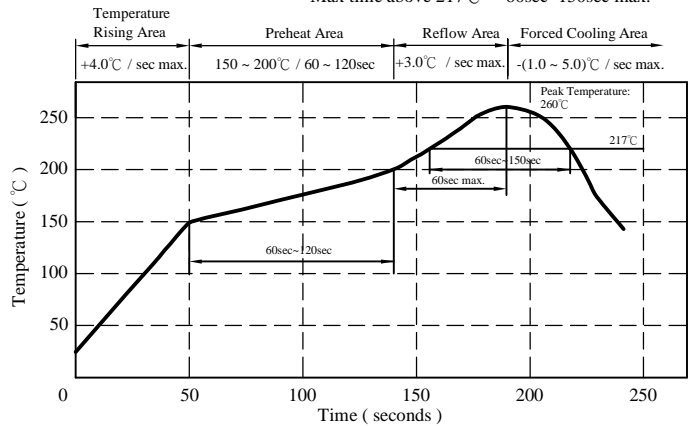
II . Description :

- a . Ferrite drum core construction.
- b . Magnetically shielded.
- c . Enamelled copper wire : H class
- d . Product weight : 0.210g (ref.)
- e . Moisture sensitivity Level 1
- f . Products comply with RoHS' requirements
- g . Halogen free available

Peak Temp : 260°C max.
Max. Peak Temp - 5°C : 30sec max.
Max time above 217°C : 60sec~150sec max.

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 260°C.10 secs.



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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	DH5018□□□□F□-□□□		
		REV.	20150824-C	PAGE	2

IV . Electrical characteristics :

DWG NO.	Inductance (μH)	RDC (mΩ) max.	Isat (A)	Irms (A)
DH50182R5YF□-□□□	2.5 ±30 %	36	2.35	2.68
DH50183R9YF□-□□□	3.9 ±30 %	58	1.80	2.30
DH50184R7YF□-□□□	4.7 ±30 %	65	1.65	2.10
DH50186R2YF□-□□□	6.2 ±30 %	84	1.40	1.80
DH50189R0YF□-□□□	9.0 ±30 %	119	1.20	1.50
DH5018100MF□-□□□	10.0 ±20 %	135	1.15	1.42
DH5018120MF□-□□□	12.0 ±20 %	170	1.00	1.25
DH5018150MF□-□□□	15.0 ±20 %	205	0.90	1.15
DH5018180MF□-□□□	18.0 ±20 %	233	0.85	1.12
DH5018220MF□-□□□	22.0 ±20 %	309	0.75	0.86
DH5018270MF□-□□□	27.0 ±20 %	359	0.72	0.95
DH5018330MF□-□□□	33.0 ±20 %	454	0.63	0.85
DH5018390MF□-□□□	39.0 ±20 %	575	0.55	0.75
DH5018470MF□-□□□	47.0 ±20 %	638	0.53	0.65
DH5018560MF□-□□□	56.0 ±20 %	835	0.45	0.62
DH5018680MF□-□□□	68.0 ±20 %	911	0.43	0.50
DH5018820MF□-□□□	82.0 ±20 %	1100	0.40	0.45
DH5018101MF□-□□□	100.0 ±20 %	1250	0.35	0.42

- 1). □ : Packaging information : □ Code
- 2). "-□□□" : Reference code
- 3). Electrical specifications at 25°C
- 4). Inductance Test Condition. : 100kHz / 0.1V
- 5). Irms base on Temp. rise 40°C typ.
- 6). Isat base on $\Delta L/L0A = 35\%$ typ.

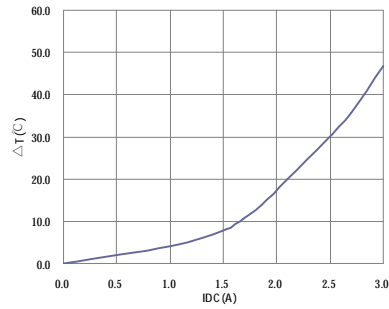
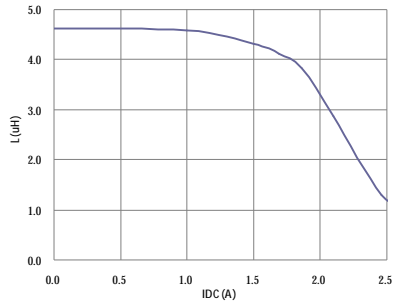
SPECIFICATION FOR APPROVAL

REF. :

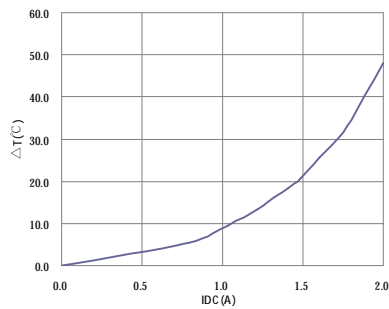
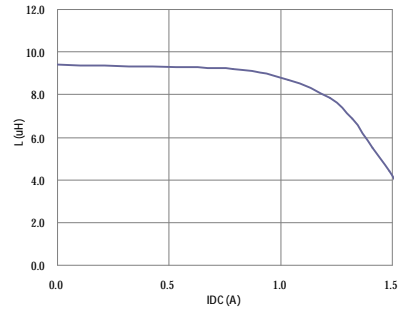
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	DH5018□□□□F□-□□□		
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V . Curve :

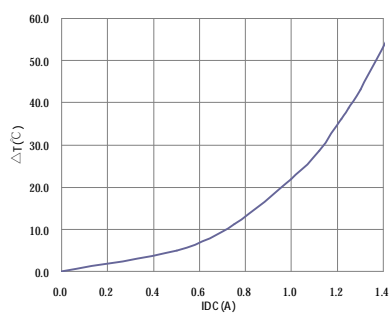
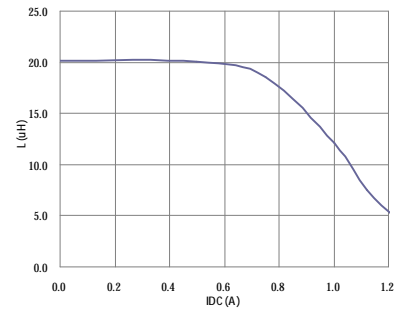
DH50184R7YF□



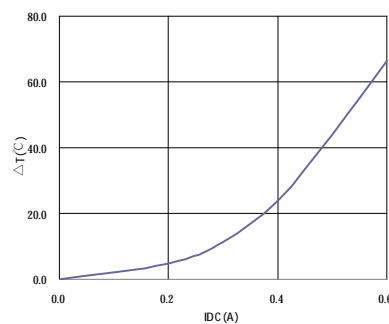
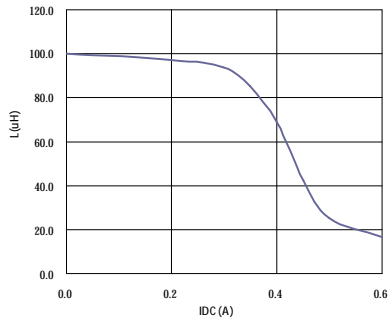
DH5018100MF□



DH5018220MF□



DH5018101MF□



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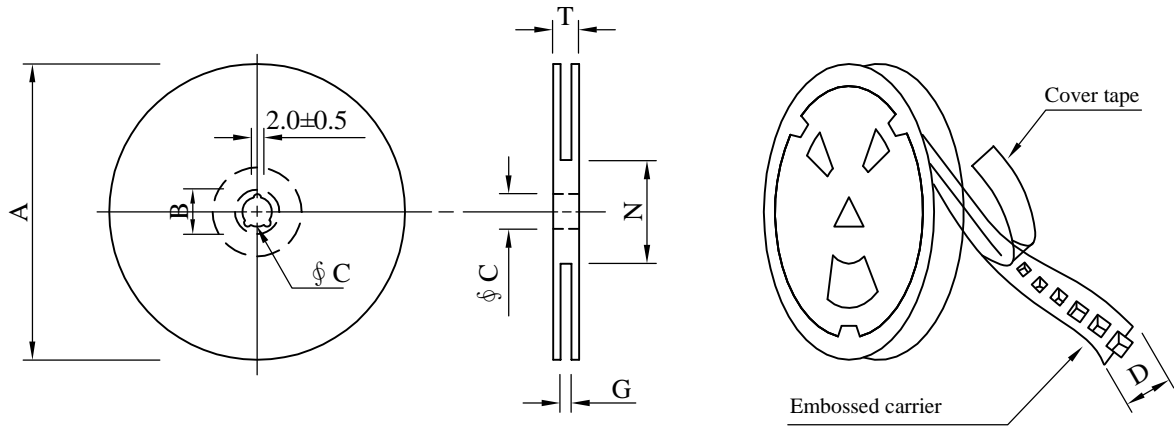
SPECIFICATION FOR APPROVAL

REF. :

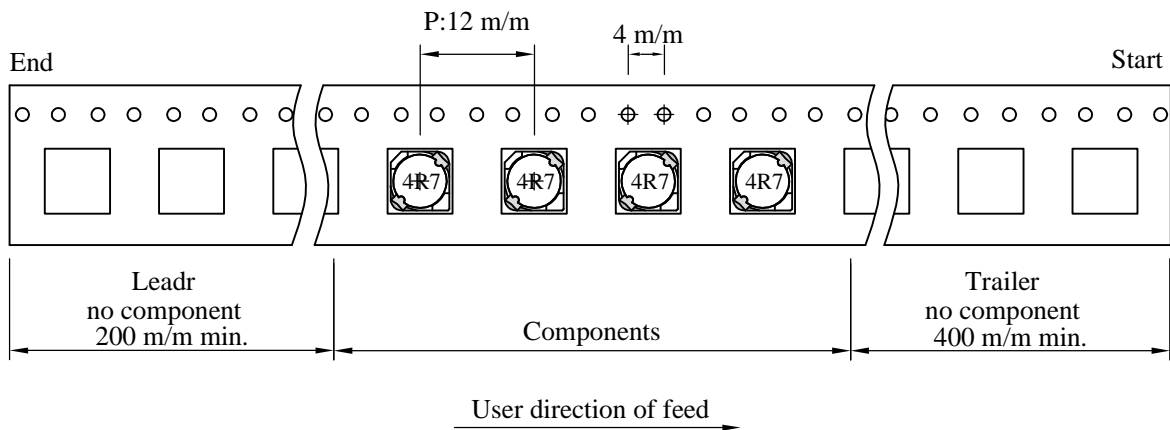
PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	DH5018□□□□F□-□□□		
		REV.	20150824-C	PAGE	4

VI . Packaging information :

(1) Configuration



※Carrier tape width : D



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
07 - 16	178	21±0.8	13	16	18 ⁺⁰	50 ⁻⁰	20.5

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	500	220	07 - 16	15,000	8.1	42 x 41 x 24

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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	Shielded SMD Power Inductor	ABC'S DWG NO.	DH5018□□□□F□-□□□		
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VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125±2°C 2.Time:96±2 hours.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
2.Temperature Cycling	JESD22-A 104	1.Temperature: -40°C ~ +125°C 2.Number of cycle:100 cycle 3.Dwell time:30 minutes	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature : 85±2 °C 2.Humidity: 85% RH. 3.Time:96±2 Hours	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
4.Operational Life	JESD22-A 108	1.Temperature: 125°C (Temp. rise included) 2.Time:96±2 hours. 3.Rated current	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
5.External Visual	JESD22-B 101 & MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22-B 100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in appearance. 2.No marking blurred. 3.Inductance shall not change more than ±20%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210 & J-STD020D.1	1.Highest temperature : 260±5°C. 2.Time (temp. ≥ 217°C) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
10.Saturation Current	JIS C 6436 & User SPEC.	1.Applied rated current for 5 second. 2.Saturation current	Inductance shall not drop more than 35% typ.
11.Over load	JIS C 6436 & User SPEC.	1.Applied one and half rated current for a period of 5 minutes. 2.Rated current	No electrical or mechanical damage
12.Temperature Rise Current	JIS C 6436 & User SPEC.	1.Applied rated current for 10 minutes. 2.Temperature measure by digital surface thermometer. 3.Irms current	Surface temperature rise is less than 40°C typ.
13.Solderability Test	J-STD-002 & JESD22-B 102	1.Baking in pre-testing : 150±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp. ≥ 217°C) : 60~150 second. 4.IR reflow times : 1 times.	More than 95% soldering coverage min on terminations.
14.Electrical Characteriazation	MIL-STD-202 Method 304 & User SPEC.	1.Operating temperature : -40°C~125°C 2.Room temperature : 25°C .	1.No mechanical or electrical damage. 2.Inductance shall not change more than ±20%.
15.Drop	CNS-C6354 & GB/T 2423.8	1.Products shall be mounted on SPEC. PCB and dropped down from a height of 1m 2.Drop total time : 6 time (Every side of sample drop 2 time)	1. Adhesion on PCB shall be enough. 2. Product appearance shall not break. 3. No electrical damage.
16.Terminal Strength Test	IEC 60068-2-21	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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