

# **MULTI-INNO TECHNOLOGY CO., LTD.**

www.multi-inno.com

# LCD MODULE SPECIFICATION

Model: MI0430H5T-2CP3

This module uses ROHS material

### For Customer's Acceptance:

	-
Customer	
Approved	
Comment	

The standard product specification may change without prior notice in order to improve performance or quality. Please contact Multi-Inno for updated specification and product status before design for the standard product or release of the order.

Revision	1.0
Engineering	
Date	2017-04-27
Our Reference	



# **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REVISED PAGE NO.
1.0	2017-04-27	First Release	



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# **■ GENERAL INFORMATION**

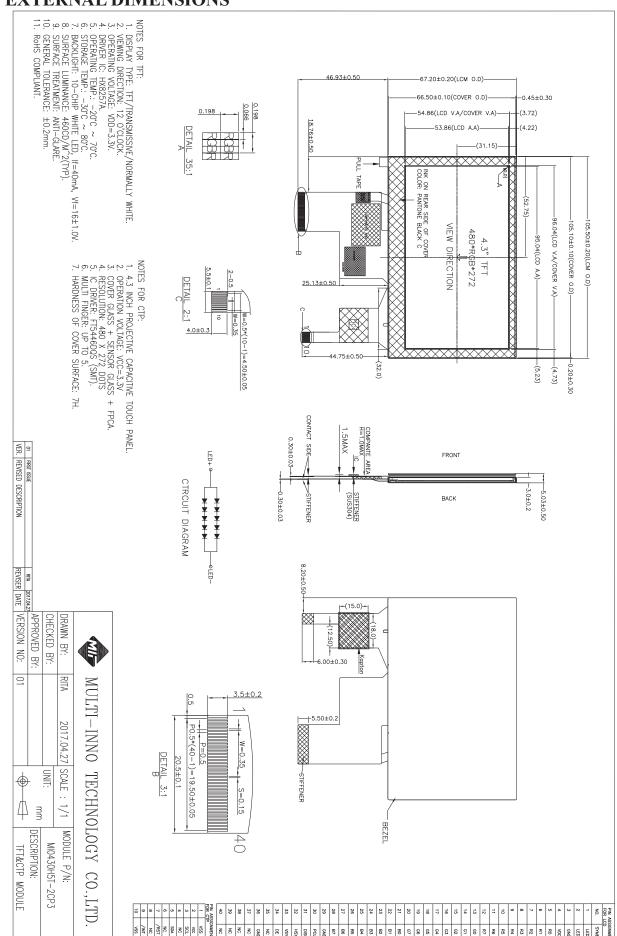
Item	Contents	Unit
LCD type	TFT/Transmissive/Normally white	/
Size	4.3	Inch
Viewing direction	12:00(without image inversion and least brightness	O' Clock
	change)	
Gray scale inversion direction	6:00 (contrast peak located at)	O' Clock
$LCM(W \times H \times D)$	105.50×67.20×5.03	$mm^3$
Active area (W×H)	95.04×53.856	$mm^2$
Pixel pitch (W×H)	0.198×0.198	mm <sup>2</sup>
Number of dots	480 (RGB) × 272	/
Driver IC	HX8257A	/
Backlight type	10 LEDs	/
Interface type	24bit RGB	/
Color depth	16.7M	/
Pixel arrangement	RGB vertical stripe	/
Input voltage	3.3	V
With/Without TSP	With CTP	/
Weight	TBD	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.



#### ■ EXTERNAL DIMENSIONS





#### ■ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Supply voltage	VDD	-0.3	4.5	V
Input voltage	VIN	GND-0.5	VDD	V
Operating temperature	Тор	-20	70	°C
Storage temperature	Tst	-30	80	°C

#### **■ELECTRICAL CHARACTERISTICS**

Parameter	Symbol	Min	Тур	Max	Unit
Supply voltage for logic	VDD	3.0	3.3	3.6	V
Input current	IDD	-	19.5	-	mA
Input voltage 'H'level	V <sub>IH</sub>	0.7VDD	-	VDD	V
Input voltage 'L' level	VIL	GND	-	0.3VDD	V

#### ■ BACKLIGHT CHARACTERISTICS

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward voltage	Vf	-	16.0	17.0	V	Ta=25±2°C,
Forward current	If	-	40	60	mA	, i
Power consumption	WBL	-	640	1020	mW	60%RH±5%
Operating life time	-	30000	50000	-	Hrs	

#### Note:

Operating life time means brightness goes down to 50% initial brightness;

The life time of LED will be reduced if LED is driven by high current, high ambient temperature and humidity conditions;

Typical operating life time is an estimated data.



#### **■ELECTRO-OPTICAL CHARACTERISTICS**

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark	Note
Response	time	Tr+Tf		-	20	-	ms	FIG 1.	4
Contrast r	atio	Cr	θ=0°	320	400	-		FIG 2.	1
Luminar uniform		δ WHITE	Ø=0° Ta=25°C	80	-	-	%	FIG 2.	3
Surface Lum	inance	Lv		360	460	-	cd/m <sup>2</sup>	FIG 2.	2
			Ø = 90°	35	50	-	deg	FIG 3.	
Vioving and	a ranga	θ	Ø = 270°	55	70	-	deg	FIG 3.	6
v lew ing angi	Viewing angle range	Ð	$\emptyset = 0$ °	55	70	-	deg	FIG 3.	] 0
			Ø = 180°	55	70	-	deg	FIG 3.	
	Red	X		0.570	0.620	0.670			
	Reu	у		0.294	0.344	0.394			
	Green	X	θ=0°	0.256	0.306	0.356			
CIE (x, y)	Green	у	Ø=0°	0.513	0.563	0.613		FIG 2.	5
chromaticity	chromaticity Blue	X	Ta=25℃	0.083	0.133	0.183		110 2.	
		у	1 a-25 C	0.099	0.149	0.199			
137	White	X		0.250	0.300	0.350			
	vv iiite	у		0.280	0.330	0.380			

Note 1. Contrast Ratio(CR) is defined mathematically as For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Note 3. The uniformity in surface luminance  $, \delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 2.

δ WHITE = Minimum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

Maximum Surface Luminance with all white pixels (P1, P2, P 3, P4, P5)

- Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope. Series.
- Note 5. CIE (x, y) chromaticity, The x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.
- Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the conrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.
- Note 7. For viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments For contrast ratio, Surface Luminance, Luminance uniformity, CIE The test data is base on TOPCON's BM-5 photo detector.



#### FIG. 1 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

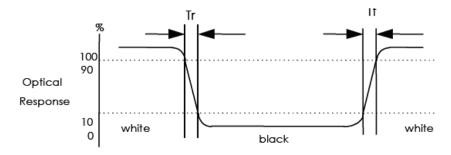
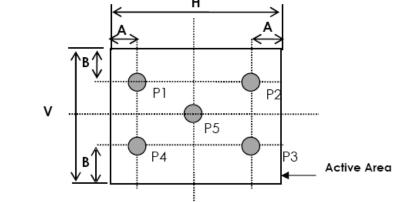


FIG. 2 Measuring method for Contrast ratio, surface luminance, Luminance uniformity , CIE (x, y) chromaticity

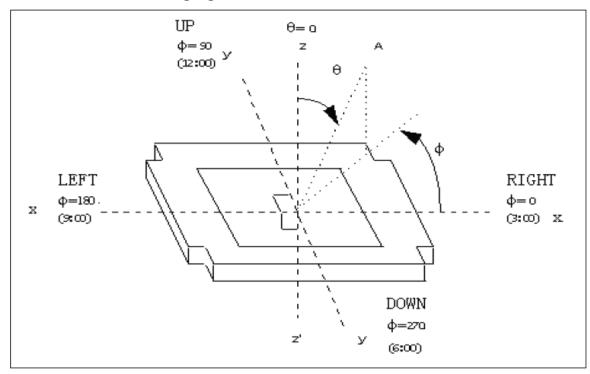


A: 5 mm B: 5 mm

H,V: Active Area

Light spot size ∅=7mm, 500mm distance from the LCD surfade to detector lens measurement instrument is TOPCON's luminance meter BM-5

#### FIG. 3 The definition of viewing angle





# **■INTERFACE DESCRIPTION**

1	Pin No.	Symbol	Description
3	1	LED-	Cathode of LED backlight
4	2	LED+	Anode of LED backlight
5         R0         Red data(LSB)           6         R1         Red data           7         R2         Red data           8         R3         Red data           9         R4         Red data           10         R5         Red data           11         R6         Red data           11         R6         Red data           11         R6         Red data           12         R7         Red data           12         R7         Red data(LSB)           13         G0         Green data           15         G2         Green data           16         G3         Green data           17         G4         Green data           18         G5         Green data           19         G6         Green data           20         G7         Green data(LSB)           21         B0         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6 <td< td=""><td>3</td><td>GND</td><td>Power ground</td></td<>	3	GND	Power ground
6 R1 Red data 7 R2 Red data 8 R3 Red data 9 R4 Red data 10 R5 Red data 11 R6 Red data 11 R6 Red data 12 R7 Red data(SB) 13 G0 Green data(LSB) 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 19 G6 Green data 19 G6 Green data 20 G7 Green data(MSB) 21 B0 Blue data 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data 29 GND Power ground 30 PCLK Pixel Clock, data sampling at the PCLK falling edge DISP = "Low"; Standby; (Default) DISP = "High": Normal display 32 HSYN Horizontal syne signal 33 VSYNC Vertical syne signal 34 DE Data input enable 35 NC No connection 38 NC No connection 39 NC No connection	4	VDD	Power supply
7         R2         Red data           8         R3         Red data           9         R4         Red data           10         R5         Red data           11         R6         Red data           11         R6         Red data(MSB)           12         R7         Red data(MSB)           13         G0         Green data (LSB)           14         G1         Green data           15         G2         Green data           16         G3         Green data           17         G4         Green data           18         G5         Green data           19         G6         Green data           20         G7         Green data(MSB)           21         B0         Blue data(LSB)           21         B0         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           28         B7         Blue data           28         B7         Blue data (MSB)           29	5	R0	Red data(LSB)
8         R3         Red data           9         R4         Red data           10         R5         Red data           11         R6         Red data           11         R6         Red data           11         R6         Red data           12         R7         Red data(MSB)           13         G0         Green data           14         G1         Green data           15         G2         Green data           16         G3         Green data           17         G4         Green data           18         G5         Green data           20         G7         Green data(MSB)           21         B0         Blue data(LSB)           22         B1         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge	6	R1	Red data
9 R4 Red data 10 R5 Red data 11 R6 Red data 11 R6 Red data 11 R7 Red data(MSB) 13 G0 Green data(LSB) 14 G1 Green data 15 G2 Green data 16 G3 Green data 17 G4 Green data 19 G6 Green data 19 G6 Green data 20 G7 Green data 20 G7 Green data 21 B0 Blue data(LSB) 22 B1 Blue data 23 B2 Blue data 24 B3 Blue data 25 B4 Blue data 26 B5 Blue data 27 B6 Blue data 28 B7 Blue data 29 GND Power ground 30 PCLK Pixel clock ,data sampling at the PCLK falling edge Display control / standby; (Default) DISP = "High": Normal display 32 HSYN Horizontal syne signal 33 VSYNC Vertical syne signal 34 DE Data input enable 35 NC No connection 38 NC No connection 39 NC No connection	7	R2	Red data
10	8	R3	Red data
11	9	R4	Red data
12	10	R5	Red data
13	11	R6	Red data
14         G1         Green data           15         G2         Green data           16         G3         Green data           17         G4         Green data           18         G5         Green data           19         G6         Green data           20         G7         Green data(MSB)           21         B0         Blue data(MSB)           22         B1         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock, data sampling at the PCLK falling edge           Dispay control / standby; (Default)         DISP = "Low": Standby; (Default)           DISP = "High": Normal display           32         HSYN         Horizontal sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37	12	R7	Red data(MSB)
15   G2   Green data     16   G3   Green data     17   G4   Green data     18   G5   Green data     19   G6   Green data     20   G7   Green data     21   B0   Blue data(LSB)     22   B1   Blue data     23   B2   Blue data     24   B3   Blue data     25   B4   Blue data     26   B5   Blue data     27   B6   Blue data     28   B7   Blue data     29   GND   Power ground     30   PCLK   Pixel clock ,data sampling at the PCLK falling edge     31   DISP   DISP = "Low": Standby; (Default)     DISP = "High": Normal display     32   HSYN   Horizontal sync signal     33   VSYNC   Vertical sync signal     34   DE   Data input enable     35   NC   No connection     36   GND   Power ground     37   NC   No connection     38   NC   No connection     39   NC   No connection	13	G0	Green data(LSB)
16         G3         Green data           17         G4         Green data           18         G5         Green data           19         G6         Green data           20         G7         Green data(MSB)           21         B0         Blue data(LSB)           22         B1         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge           Display control / standby mode selection         DISP = "Low": Standby; (Default)           DISP = "High": Normal display           32         HSYN         Horizontal sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37         NC         No connection           38         NC         No connection	14	G1	Green data
17         G4         Green data           18         G5         Green data           19         G6         Green data           20         G7         Green data(MSB)           21         B0         Blue data(LSB)           22         B1         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge           31         DISP         "Low": Standby mode selection           DISP         "Low": Standby; (Default)           DISP         "High": Normal display           32         HSYN         Horizontal sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37         NC         No connection           38         NC         No connection           39	15	G2	Green data
18         G5         Green data           19         G6         Green data           20         G7         Green data(MSB)           21         B0         Blue data(LSB)           22         B1         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge           31         DISP         "Low": Standby mode selection           DISP         "Low": Standby; (Default)           DISP         "High": Normal display           32         HSYN         Horizontal sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37         NC         No connection           38         NC         No connection           39         NC         No connection	16	G3	Green data
19	17	G4	Green data
20   G7   Green data(MSB)	18	G5	Green data
21	19	G6	Green data
22         B1         Blue data           23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge           31         DISP         "Low": Standby mode selection           DISP = "Low": Standby; (Default)         DISP = "High": Normal display           32         HSYN         Horizontal sync signal           33         VSYNC         Vertical sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37         NC         No connection           38         NC         No connection           39         NC         No connection	20	G7	Green data(MSB)
23         B2         Blue data           24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge           Display control / standby mode selection         DISP = "Low": Standby; (Default)           DISP = "High": Normal display           32         HSYN         Horizontal sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37         NC         No connection           38         NC         No connection           39         NC         No connection	21	В0	Blue data(LSB)
24         B3         Blue data           25         B4         Blue data           26         B5         Blue data           27         B6         Blue data(MSB)           29         GND         Power ground           30         PCLK         Pixel clock ,data sampling at the PCLK falling edge           31         DISP         DISP = "Low" : Standby mode selection           DISP = "Low" : Standby; (Default)         DISP = "High" : Normal display           32         HSYN         Horizontal sync signal           34         DE         Data input enable           35         NC         No connection           36         GND         Power ground           37         NC         No connection           38         NC         No connection           39         NC         No connection	22	B1	Blue data
25	23	B2	Blue data
26 B5 Blue data  27 B6 Blue data  28 B7 Blue data(MSB)  29 GND Power ground  30 PCLK Pixel clock ,data sampling at the PCLK falling edge  Display control / standby mode selection  DISP DISP "Low": Standby; (Default)  DISP "High": Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	24	В3	Blue data
27 B6 Blue data  28 B7 Blue data(MSB)  29 GND Power ground  30 PCLK Pixel clock ,data sampling at the PCLK falling edge  31 DISP Display control / standby mode selection  31 DISP "Low": Standby; (Default)  DISP "High": Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	25	B4	Blue data
28 B7 Blue data(MSB)  29 GND Power ground  30 PCLK Pixel clock ,data sampling at the PCLK falling edge  Display control / standby mode selection  DISP DISP = "Low": Standby; (Default)  DISP = "High": Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	26	B5	Blue data
29 GND Power ground 30 PCLK Pixel clock ,data sampling at the PCLK falling edge  Display control / standby mode selection DISP = "Low" : Standby; (Default) DISP = "High" : Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	27	В6	Blue data
30 PCLK Pixel clock ,data sampling at the PCLK falling edge  Display control / standby mode selection  DISP "Low": Standby; (Default)  DISP = "High": Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	28	В7	Blue data(MSB)
Display control / standby mode selection DISP = "Low": Standby; (Default) DISP = "High": Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	29	GND	Power ground
DISP DISP "Low": Standby; (Default) DISP = "High": Normal display  32 HSYN Horizontal sync signal  33 VSYNC Vertical sync signal  34 DE Data input enable  35 NC No connection  36 GND Power ground  37 NC No connection  38 NC No connection  39 NC No connection	30	PCLK	Pixel clock ,data sampling at the PCLK falling edge
33 VSYNC Vertical sync signal 34 DE Data input enable 35 NC No connection 36 GND Power ground 37 NC No connection 38 NC No connection 39 NC No connection	31	DISP	DISP = "Low": Standby; (Default)
34 DE Data input enable 35 NC No connection 36 GND Power ground 37 NC No connection 38 NC No connection 39 NC No connection	32	HSYN	Horizontal sync signal
35 NC No connection 36 GND Power ground 37 NC No connection 38 NC No connection 39 NC No connection	33	VSYNC	Vertical sync signal
36 GND Power ground 37 NC No connection 38 NC No connection 39 NC No connection	34	DE	Data input enable
37 NC No connection 38 NC No connection 39 NC No connection	35	NC	No connection
38 NC No connection 39 NC No connection	36	GND	Power ground
39 NC No connection	37	NC	No connection
	38	NC	No connection
40 NC Na compaction	39	NC	No connection
10 INO connection	40	NC	No connection

# ■ APPLICATION NOTES

# 1 Timing characteristics

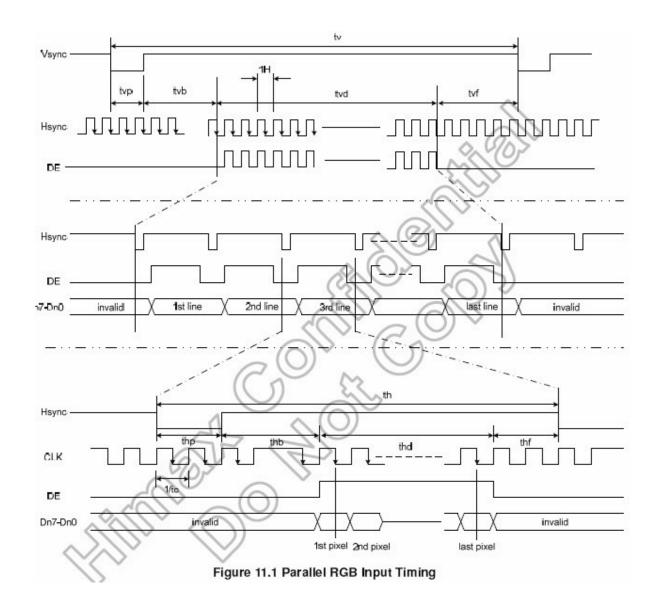
Parameter	Symbol		Spec.	Spec.		
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Clock cycle	f <sub>CLK</sub> <sup>(1)</sup>	-	7/00	15	MHz	
Hsync cycle	1/th	-\	(17.14	.<	KHz	
Vsync cycle	1/tv	8	59.94	2	) Hz	
Horizontal Signal		50		70	-	
Horizontal cycle	th	525	525	605	CLK	
Horizontal display period	thd	480	480	480	CLK	
Horizontal front porch	thf	\\\\2	((21)	82	CLK	
Horizontal pulse width	thp <sup>(2)</sup>	) 2	41/	41	CLK	
Horizontal back porch	(thb(2)	2(>)	2	41	CLK	
Vertical Signal		4	5			
Vertical cycle	tv	/285	286	511	H <sup>(1)</sup>	
Vertical display period	tvd	272	272	272	H <sup>(1)</sup>	
Vertical front porch	tvf	4/1	2	227	H <sup>(1)</sup>	
Vertical pulse width	tvp <sup>(2)</sup>	71	10	11	H <sup>(1)</sup>	
Vertical back porch	tvb <sup>(2)</sup>	1	2	- 11	H <sup>(1)</sup>	

Note: (1) Unit: CLK=1/ f<sub>CLK</sub>, H=th,
(2) It is necessary to keep tvp+tvb=12 and thp+thb=43 in sync mode. DE mode is unnecessary to keep it.

Parameter	Cumbal		Spec.	Spec.		
Parameter	Symbol	Min.	Тур.	yp. Max.		
Clock cycle	f <sub>CLK</sub> <sup>(1)</sup>	-	9.6	15	MHz	
Hsync cycle	1/th	-	15.72	-	KHz	
Vsync cycle	1/tv	-	60	-	Hz	
Horizontal Signal	89 99	50	<del>7</del>	5 55	b	
Horizontal cycle	th	525	612		CLK	
Horizontal display period	thd	480	480	480	CLK	
Horizontal front porch	thf	2	30	-	CLK	
Horizontal pulse width	thp	2	46	-	CLK	
Horizontal back porch	thb	2	56	125	CLK	
Vertical Signal	•					
Vertical cycle	Tv	3570	262	275	H <sup>(1)</sup>	
Vertical display period	Tvd	-	240	-	H <sup>(1)</sup>	
Vertical front porch	Tvf	1	4	-	H <sup>(1)</sup>	
Vertical pulse width	tvp	1	3	-	H <sup>(1)</sup>	
Vertical back porch	tvb	1	15	-	H <sup>(1)</sup>	

Note: (1) Unit: CLK=1/ fclk, H=th,







#### ■ CTP SPECIFICATIONS

#### 1. GENERAL SPECIFICATIONS

Item	Specification	Unit
Туре	Projective capacitive type touch panel	
Strueture	Cover glass+Sensor glass+FPCA	
Input mode	Human's finger	
Finger	Up to 5	
Resolution	480 x 272	dots
Cover viewing area	96.04(W) x 54.86(H)	mm
Sensor Active Area	97.04(W) x 55.86(H)	mm
Hardness	7H	Pressure 750g force, 45 degree
Driver IC	FT5446	1

#### 2. ABSOLUTE MAXIMUM RATINGS

Symbol	Description	Min	Тур	Max	Unit	Notes
VCC	Supply voltage	2.8	3.3	3.6	V	
ICC	Supply current	-	TBD	-	mA	
VIH	Input high-level voltage	0.7VCC	-	VCC	V	
VIL	Input low-level voltage	-0.3	-	0.3VCC	V	
VOH	Output high-level voltage	0.7VCC	-	-	V	
VOL	Output low-level voltage	-	-	0.3VCC	V	

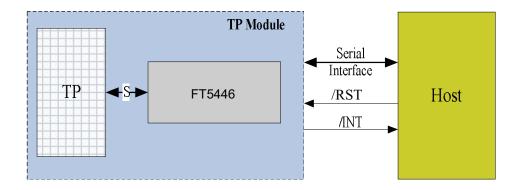
#### 3. PIN CONNECTIONS

No.	Name	I/O	Description	
1	VSS	Р	Ground	
2	VCC	Р	Power supply	
3	SCL	I/O	I2C clock signal	
4	NC	-	No connection	
5	SDA	I/O	I2C data signal	
6	NC	-	No connection	
7	/RST	I/O	Reset.active low	
8	NC	-	No connection	
9	/INT	I/O	Interrupt signal to host from CTP	
10	VSS	Р	Ground	

Note: SCL/SDA for I2C in terface internal pull up on pin  $3\&5(100\mbox{K}\Omega)$ 



#### 4. BLOCK DIAGRAM



#### 5. CTP TIMING

The I2C is always configured in the Slave mode. The data transfer format is shown in Figure 1-1.

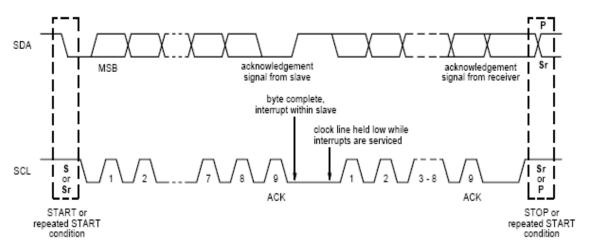


Figure 1-1 I2C Serial Data Transfer Format

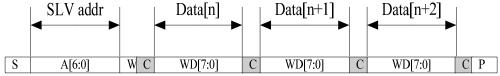


Figure 1-2 I2C master write, slave read

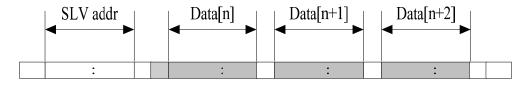


Figure 1-3 I2C master read, slave write

Table 1-1 lists the meanings of the mnemonics used in the above figures.



**Table 1-1 Mnemonics Description** 

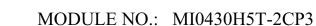
Mnemonics	Description	
S	I2C Start or I2C Restart	
A[6:0]	Slave address A[6:4]: 3'b011 A[3:0]: data bits are identical to those of I2CCON[7:4] register.	
W	1'b0: Write	
R	1'b1: Read	
С	ACK	
P	STOP: the indication of the end of a packet (if this bit is missing, S will indicate the end of the current packet and the beginning of the next packet)	

I2C Interface Timing Characteristics is shown in Table 1-2.

**Table 1-2 I2C Timing Characteristics** 

Parameter	Unit	Min	Max
SCL frequency	KHz	0	400
Bus free time between a STOP and START condition	us	4.7	\
Hold time (repeated) START condition	us	4.0	\
Data setup time	ns	250	\
Setup time for a repeated START condition	us	4.7	\
Setup Time for STOP condition	us	4.0	\

Note: More information pls refer to IC spec.





# **■ RELIABILITY TEST**

No.	Test Item	Test Condition	Remarks
1	High Temperature Storage Test	$80^{\circ}\text{C} \pm 2^{\circ}\text{C}/240\text{Hrs}.$	Note2
2	Low Temperature Storage Test	$-30^{\circ}\text{C} \pm 2^{\circ}\text{C}/240\text{Hrs}.$	Note1,2
3	High Temperature Operation Test	70°C±2°C/240Hrs.	
4	Low Temperature Operation Test	-20°C±2°C/240Hrs.	Note1
5	High Temperature and High Humidity Operation Test	60±5℃, 90%RH 240Hrs.	Note1,2
6	Thermal Shock Test	-30±2°C(30Min.)~25±2°C(5Min.)~80±2°C(30Min.)	
	(Non-operating)	10Cycles	
7	Vibration Test (Non-operating)	Frequency:10~55Hz Amplitude: 1.5mm Sweep Time: 11Mins Test Period: 6 Cycles For Each Direction Of X, Y, Z (Packing Condition)	
8	Shock Test (Non-operating)	100G, 6Ms Direction: ±X, ±Y, ±Z Cycle: 3 Times	
9	Electro Static Discharge Test (Non-operating)	Voltage: ±8KV, R:330Ω, C:150pF, Air Discharge, 10 Times. (Packing Condition)	

Note 1: Without water condensation

Note 2: The function test shall be conducted after 2 hours storage at the room temperature and humidity after removed from the test chamber.



#### ■ INSPECTION CRITERION

MI	OUTGOING QUALITY STANDARD	PAGE 1 OF 5
TITLE:FUNCTION	AL TEST & INSPECTION CRITERIA	

This specification is made to be used as the standard acceptance/rejection criteria for TFT module.

1 Sample plan

1.1 Lot size: Quantity per shipment lot per model

1.2 Sampling type: Normal inspection, Single sampling

1.3 Inspection level: II

1.4 Sampling table: MIL-STD-105D1.5 Acceptable quality level (AQL)

Major defect: AQL=0.65 Minor defect: AQL=1.50

2. Inspection condition

2.1 Ambient conditions:

a. Temperature: Room temperature  $25\pm5^{\circ}$ C

b. Humidity: (60± 10) %RH

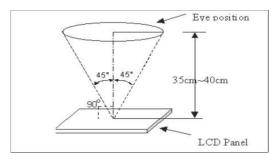
c. Illumination: Single fluoresænt lamp non-directive (300 to 700 Lux)

2.2 Viewing distance:

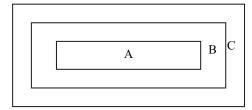
The distance between the LCD and the inspector's eyes shall be at least  $35\pm$  5cm.

2.3 Viewing Angle

U/D: 45° /45° , L/R: 45° /45°



- 3. Definition of Inspection Item.
  - 3.1 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.





#### OUTGOING QUALITY STANDARD

PAGE 2 OF 5

#### TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### 4. Inspection standards

Defects are classified as major defects and minor defects according to the degree of defectiveness defined herein.

#### 4.1 Major defect

Item No	Items to be inspected	Inspection Standard
4.1.1	All functional defects	<ol> <li>No display</li> <li>Display abnormally</li> <li>Short circuit</li> <li>Line defect</li> <li>Excess power consumption</li> </ol>
4.1.2	Missing	Missing function component
4.1.3	Crack	Glass crack

#### 4.2 Minor defect

Item No	Items to be inspected	Inspection standard	
4.2.1	Spot Defect Including Black spot	For dark/white spot is def $\varphi = (\mathbf{x} + \mathbf{y}) / 2$ $\longrightarrow \mathbf{X} \qquad \qquad$	ined
	White spot	Size φ(mm)	Acceptable Quantity
	Pinhole Foreign particle	φ≤0.15 2mm(min) apart	Ignore
	Polarizer dirt	0.15 < φ≤ 0.25 5mm(min) apart	3
		0.25<φ	Not allowed



MIT	OUTGOI	NG QUALITY STANDARD	PAGE 3 OF 5
LE:FUN	CTIONAL TEST & 1	INSPECTION CRITERIA	
		Define:	Vidth
4.2.2	Line Defect Including Black line	Width(mm) Length(mm)	Acceptable Quantity
	White line	W≤0.05 and L≤10	Ignore
	Scratch	0.05 < W≤0.08 and L≤10 3mm(min) apart	3
		0.08 < W≤0.10 and L≤5 3mm(min) apart	1
		0.10< W or 10 <l< td=""><td>Not allowed</td></l<>	Not allowed
	Polarizer	Size φ(mm)	Acceptable Quantity
		φ≤0.25	Ignore
4.2.3	Dent/Bubble	Non visible area	Ignore
		0.25<φ≤ 0.50 5mm(min) apart	3
		0.50 < φ	Not allowed
		Bright and Black dot defi	and
4.2.4	Electrical Dot Defect	Inspection pattern: Full and blue screens	white, Full black, Red, green
		Item	Acceptable Quantity
		Black dot defect	2
		Bright dot defect	0
		Total Dot	2





# OUTGOING QUALITY STANDARD

PAGE 4 OF 5

# TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA

		1.Corner chips:	X y
		Size(mm)	Acceptable Quantity
4.2.5	Touch panel chips	X≤3mm Y≤3mm Z≤T  2. Side chips:	Ignore T: Glass thickness X: Length Y: Width Z: thickness
		Size(mm)	Acceptable Quantity
		X≤5mm Y≤3mm Z≤T	Ignore T: Glass thickness X: Length Y: Width Z: thickness





#### OUTGOING QUALITY STANDARD

PAGE 5 OF 5

#### TITLE:FUNCTIONAL TEST & INSPECTION CRITERIA

Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

- 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart. The distance between two bright dot defects should be more than 15mm apart
- 3. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.
- 4. Mura is checker by 6% ND filter.
- 5. Foreign particle on the surface of the LCM should be ignore.

#### ■ PRECAUTIONS FOR USING LCD MODULES

#### **Handing Precautions**

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
  - (9) Do not attempt to disassemble or process the LCD module.
  - (10) NC terminal should be open. Do not connect anything.
  - (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated



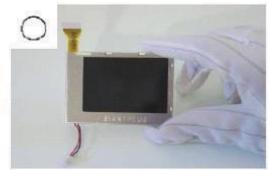
- (13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
  - Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
  - Do not damage or modify the pattern writing on the printed circuit board.
  - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
  - Do not drop, bend or twist LCM.

#### Handling precaution for LCM

LCM is easy to be damaged.
Please note below and be careful for handling!

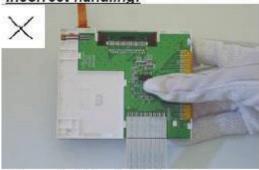
Correct handling:





As above picture, please handle with anti-static gloves around LCM edges.

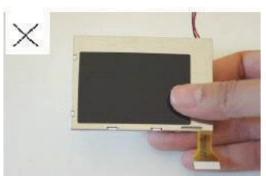
Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



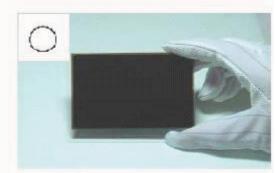
#### Handling precaution for LCD

LCD is easy to be damaged.

Please note below and be careful for handling!

#### Correct handling:





As above photo, please handle with anti-static gloves around LCD edges.

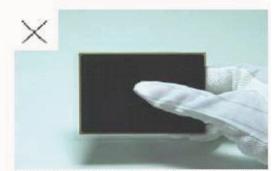
# Incorrect handling:



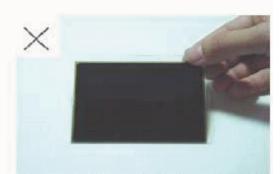
Please don't stack the LCDS.



Please don't operate with sharp stick such as pens.



Please don't hold the surface of LCD.



Please don't touch ITO glass without anti-static gloves.



#### **Storage Precautions**

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the anti-static electricity container in which they were shipped. Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

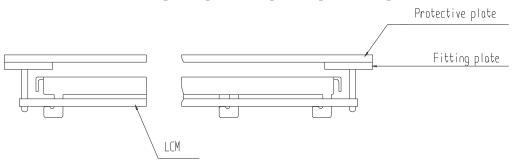
- Exposed area of the printed circuit board.
- -Terminal electrode sections.

#### USING LCD MODULES

#### **Installing LCD Modules**

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

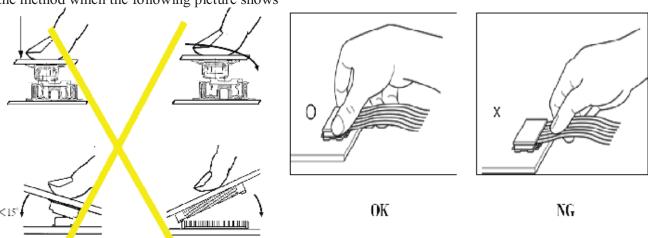
(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm.

#### Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position,don't assemble or assemble like the method which the following picture shows





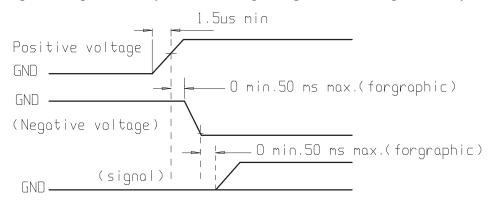
#### Precaution for soldering to the LCM

	Hand soldering	Machine drag soldering	Machine press soldering
No ROHS	290°C ~350°C.	330°C ~350°C.	300°C ~330°C.
product	Time : 3-5S.	Speed: 4-8 mm/s.	Time : 3-6S.
product			Press: 0.8~1.2Mpa
ROHS	340°C ~370°C.	350°C ~370°C.	330°C ~360°C.
product	Time : 3-5S.	Time: 4-8 mm/s.	Time : 3-6S.
product			Press: 0.8~1.2Mpa

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

#### **Precautions for Operation**

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- (2) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- (3) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature,50%RH or less is required.
  - (6) Input each signal after the positive/negative voltage becomes stable.
- (7) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.



#### **Safety**

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

#### **Limited Warranty**

Unless agreed betweenMulti-Inno and customer,Multi-Inno will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with Multi-Inno LCD acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to Multi-Inno within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of Multi-Inno limited to repair and/or replacement on the terms set forth above. Multi-Inno will not be responsible for any subsequent or consequential events.

#### **Return LCM under warranty**

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet is damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

#### ■ PRIOR CONSULT MATTER

- 1. ①For Multi-Inno standard products, we keep the right to change material, process ... for improving the product property without notice on our customer.
  - ②For OEM products, if any change needed which may affect the product property, we will consult with our customer in advance.
- 2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.