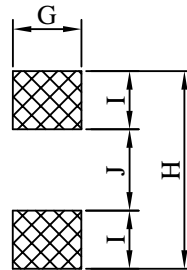
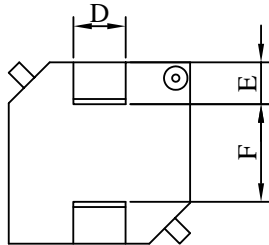
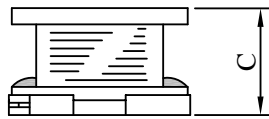
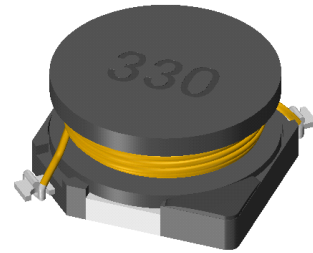
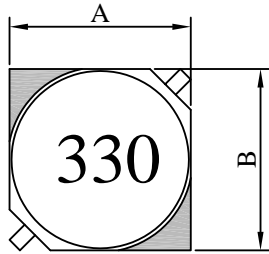


SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SB6028□□□□L□-□□□		
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I . Configuration and dimensions :



(PCB Pattern)

Unit : m/m

A	B	C	D	E	F	G	H	I	J
6.00±0.3	6.00±0.3	2.80±0.3	2.00±0.3	1.90 typ.	2.20 ref.	2.40 ref.	6.70 ref.	2.30 ref.	2.10 ref.

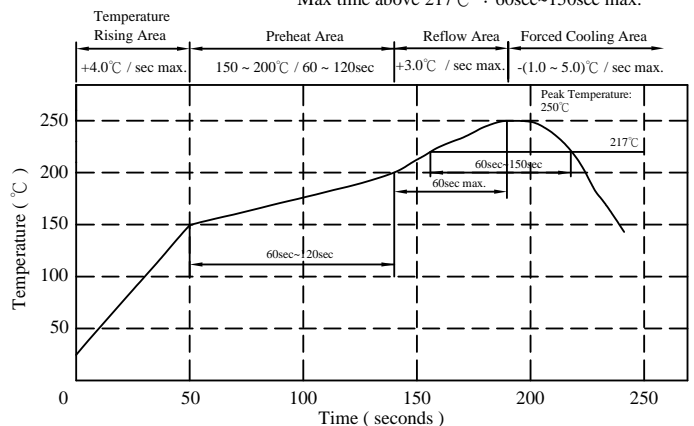
II . Description :

- a . Ferrite drum core construction.
- b . Enamelled copper wire : F class
- c . Product weight : 0.30g (ref.)
- d . Moisture sensitivity Level 1
- e . Products comply with RoHS' requirements
- f . Halogen free available.

Peak Temp : 250°C max.
Max. Peak Temp - 5°C : 30sec max.
Max time above 217°C : 60sec~150sec max.

III . General specification :

- a . Storage temp. : -40°C ----+125°C
- b . Operating temp. : -40°C ----+125°C
(Temp. rise included)
- c . Resistance to solder heat : 250°C.10 secs.



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SPECIFICATION FOR APPROVAL

REF. :

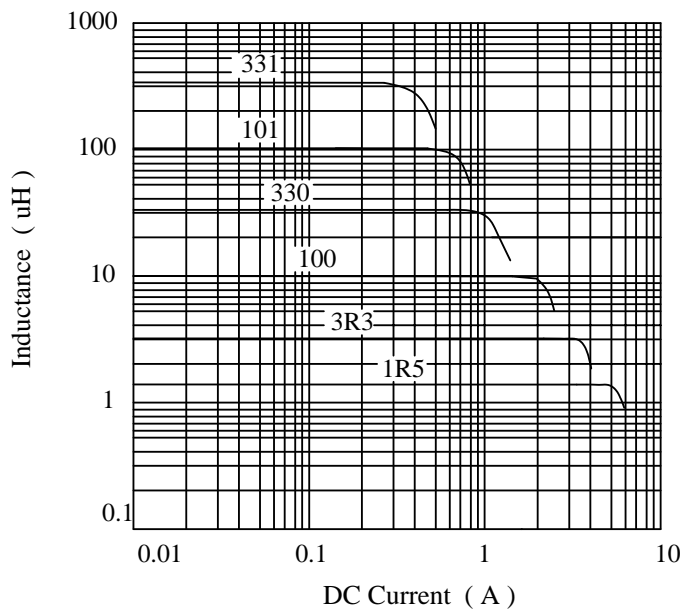
PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SB6028□□□□L□-□□□		
		REV.	20140108-B	PAGE	2

V . Electrical characteristics :

DWG No.	Inductance (μH)	Test Freq. (Hz)	RDC (Ω) max.	Irms (A)	Isat (A)
SB60281R5ML□-□□□	1.5±20%	100K/0.1V	0.028	4.00	3.50
SB60282R2ML□-□□□	2.2±20%	100K/0.1V	0.030	3.30	2.80
SB60283R3ML□-□□□	3.3±20%	100K/0.1V	0.058	2.50	2.30
SB60284R7ML□-□□□	4.7±20%	100K/0.1V	0.065	2.20	2.00
SB60286R8ML□-□□□	6.8±20%	100K/0.1V	0.085	2.00	1.70
SB6028100ML□-□□□	10.0±20%	100K/0.1V	0.115	1.70	1.50
SB6028150ML□-□□□	15.0±20%	100K/0.1V	0.160	1.40	1.15
SB6028220ML□-□□□	22.0±20%	100K/0.1V	0.210	1.25	0.95
SB6028330ML□-□□□	33.0±20%	100K/0.1V	0.320	1.00	0.80
SB6028470KL□-□□□	47.0±10%	100K/0.1V	0.450	0.75	0.70
SB6028680KL□-□□□	68.0±10%	100K/0.1V	0.650	0.65	0.60
SB6028101KL□-□□□	100.0±10%	100K/0.1V	0.880	0.52	0.50
SB6028151KL□-□□□	150.0±10%	100K/0.1V	1.280	0.40	0.38
SB6028221KL□-□□□	220.0±10%	100K/0.1V	1.920	0.33	0.30
SB6028331KL□-□□□	330.0±10%	100K/0.1V	2.850	0.28	0.26
SB6028471KL□-□□□	470.0±10%	100K/0.1V	4.350	0.22	0.21
SB6028681KL□-□□□	680.0±10%	100K/0.1V	6.500	0.18	0.18
SB6028102KL□-□□□	1000.0±10%	100K/0.1V	12.500	0.13	0.14

- | | |
|--|---|
| 1). □ : Packaging information : □ Code | 4). Inductance test freq. : 100KHz / 0.1V |
| 2). "- □□□ " : Reference code | 5). Irms base on temp. rise 40°C max. |
| 3). Electrical specifications at 25°C | 6). Isat base on ΔL/L0A=10 % max. |

@ Inductance VS. DC Superposition Characteristics



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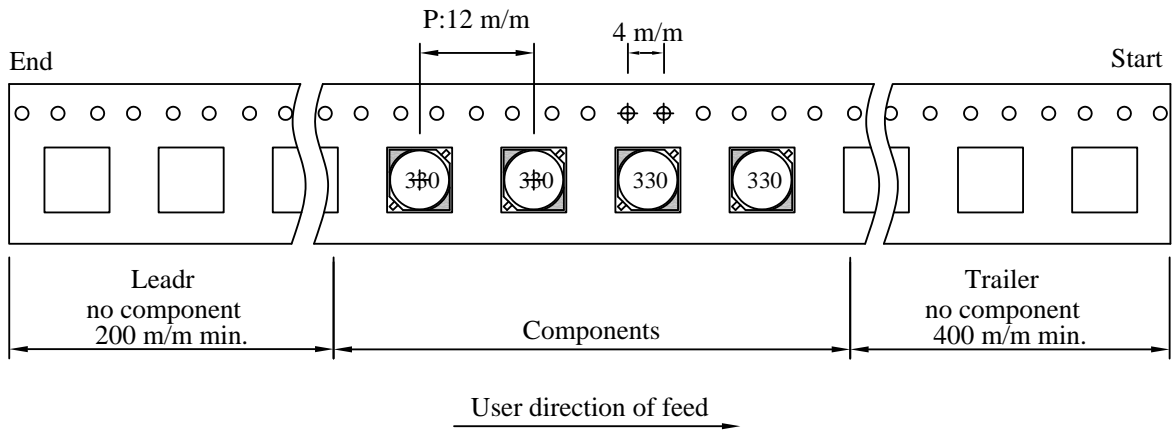
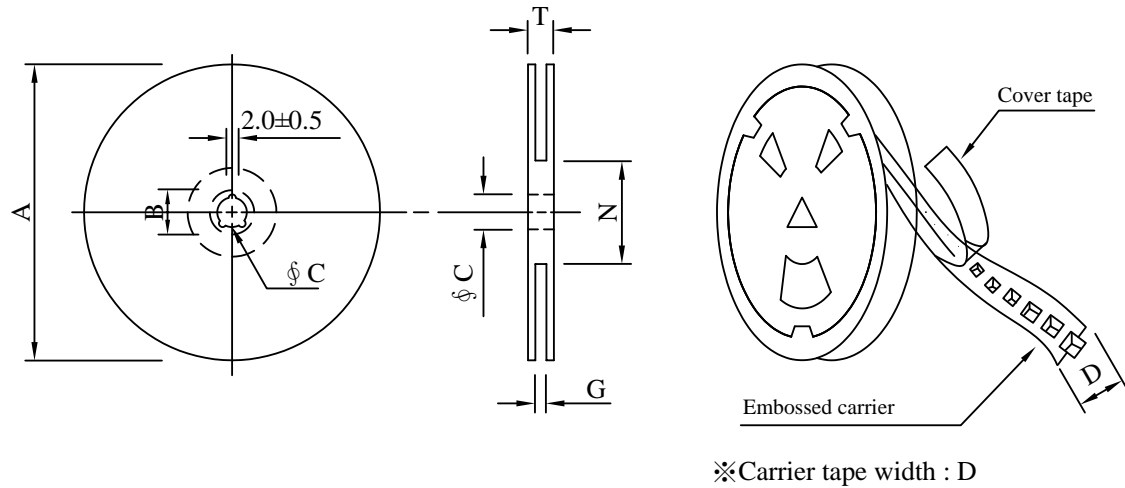
SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SB6028□□□□L□-□□□		
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VI . Packaging information :

(1) Configuration



(2) Dimensions

Unit:m/m

Style	A	B	C	D	G	N	T
07 - 16	178	21±0.8	13	16	18 ⁺⁰	50 ⁻⁰	20.5
13 - 16	330	21±0.8	13±0.5	16	18 ⁺⁰	50 ⁻⁰	22.4

(3) Q'TY & G.W. Per package

Code	Inner : Reel			Outer : Carton		
	Q'TY (pcs)	G.W. (gw)	Style	Q'TY (pcs)	G.W. (Kg)	Size (cm)
B	400	300	07 - 16	12,000	10.0	42 x 41 x 24
C	1,500	1100	13 - 16	9,000	8.0	38 x 37 x 22

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SPECIFICATION FOR APPROVAL

REF. :

PROD. NAME	SMD Power Inductor	ABC'S DWG NO.	SB6028□□□□L□-□□□		
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VII . Reliability test :

Item	Reference documents	Test Condition	Test Specification
1.High Temperature Exposure	MIL-STD-202 Method 108	1.Temperature: 125°C 2.Time:96 hours.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
2.Temperature Cycling	JESD22 Method JA-104	1.Temperature: -40°C ~ 125°C 2.Number of cycle:96 cycle 3.Dwell time:30 minutes	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
3.Biased Humidity Test	MIL-STD-202 Method 103	1.Temperature:85±5 °C 2.Time:96 Hours 3.Humidity: 85±5% RH.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
4.Operational Life	MIL-PRF-27	1.Temperature: 125°C 2.Time:96 hours. 3.Apply rated current.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
5.Exeternal Visual	MIL-STD-883 Method 2009	Inspect product constructions, marking and workmanship.	1.No pollution on the surface of products. 2.Clear marking. 3.No crack.
6.Physical Dimensions	JESD22 Method JB-100	Verify physical dimensions to the applicable product detail specification.	Per product specification standard
7.Resistance to solvents	MIL-STD-202 Method 215	Immerse into solvent for 3±0.5 minutes & brush 10 times for 3 cycles.	1.No body change in apperance. 2.No marking blurred. 3.Inductance shall not change more than ±10%.
8.Vibration Test	MIL-STD-202 Method 204	1.Frequency and Amplitued : 10-2000-10 Hz, 1.5 mm. 2.Direction:X, Y, Z 3.Test duration:2 hours for each direction, 6 hours in total.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
9.Resistance To Soldering Heat Test	MIL-STD-202 Method 210	1.Highest temperature : 250±5°C 2.Time (temp. ≥ 217°C) : 60~150 Second. 3.IR reflow times : 3 times.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
10.Rated current	MIL-STD-202 Method 330	Apply rated current for 5 second.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
11.Temperature rise	MIL-PRF-27	Apply rated current for 10 minutes.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
12.Over load	MIL-PRF-27	Apply double as rated current for 5 minutes. (It's not application to some special design)	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
13.Solderability Test	J-STD-002	1.Baking in pre-testing : 155±5°C / 16Hours±30 min. 2.Peak temperature : 240±5°C 3.Time (temp. ≥ 217°C) : 60~150 second. 4.IR reflow times : 1 times.	The terminal shall be at least 95% covered with fresh solder.
14.Electrical Characteriazation	User Spec.	1.Operating temperature : -40°C~125°C 2.Room temperature : 25°C.	1.No mechanical and electrical damage. 2.Inductance shall not change more than ±10%.
15.Withstanding Voltage Test	MIL-STD-202 Method 201	1.DC:500V 2.Time:1minutes	1.During the test no breakdown. 2.The characteristic is normal after test.
16.Drop	JESD22-B111	Packaged & Drop down from 1m.In 1 angle lridges & 2 surfaces orientation.	1.No case deformation or change in appearance. 2.Inductance shall not change more than ±10%.
17.Terminal Strength Test	JIS-C-6429	1.Apply push force to samples mounted on PCB. 2.Force of 1.8 kg for 60±1 seconds.	After test, inductors shall be no mechanical damage.

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